PATENT ABSTRACTS OF JAPAN

(11)Publication number:

58-159367

(43) Date of publication of application: 21.09.1983

(51)Int.CI.

H01L 27/04 GO4F 5/04

H01L 29/44

H01L 29/94

(21)Application number: 57-043434

(71)Applicant: MATSUSHITA ELECTRONICS CORP

(22)Date of filing:

17.03.1982

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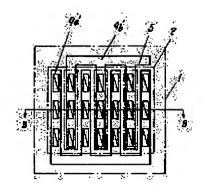
KAYAHARA MASAO

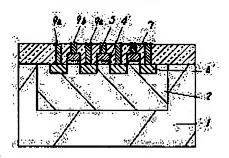
(54) MOS CAPACITOR DEVICE

(57)Abstract:

PURPOSE: To obtain a MOS capacitor device capable of firmly following the reference frequency yielded by a crystal oscillator by a method wherein contact electrodes in contact with diffused layers and polycrystalline Si film electrodes are formed into a comb-shape for reduced internal resistance and a smaller time constant.

CONSTITUTION: MOS capacitor electrodes 9a', 9b' are shaped into a comb. A P+ diffused region 6' provided in a P+ diffused layer 2 is spread in an elongated flat surface, to be connected by a second layer wiring that is an Al film 9a'. As the contact electrodes are formed in a comb-shape, effective distance is reduced for the Pdiffused layer 2 connected to the high potential terminal 9a' of the power source, which causes the internal resistance to decrease. Accordingly, the time constant is made smaller, the behavior of the complementary MOS oscillating circuit for a high frequency crystal oscillator is fully followed. A polycrystalline Si film 5 and a dielectric layer 4' immediately thereunder are also interwoven into the combshaped electrodes in parallel with the high-potential terminal 9'a for the formation of the external terminal 9'b into a comb-shaped, symmetrical configuration.





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[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]
[Date of registration]
[Number of appeal against examiner's decision of rejection]
[Date of requesting appeal against examiner's decision of rejection]
[Date of extinction of right]

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